

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,395,532 B2

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APPLICATION NO. : 10/612831

DATED : July 1, 2008

INVENTOR(S) : Antonio Maria Borneo et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

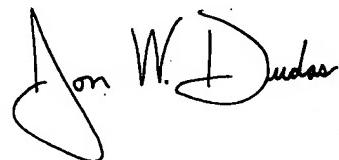
Column 14

Line 19, "processor to said instruction set stream" should read as -- processor to an instruction set stream --

Line 42, "load measurement comprises number of operations" should read as -- load measurement comprises a number of operations --

Signed and Sealed this

Twenty-first Day of October, 2008



JON W. DUDAS
Director of the United States Patent and Trademark Office